Form PTO-1449 (modified)	Atty. Docket No. 62519-00003USPT	Serial No. 10/667,035	
List pppears and Publications for Applicant's INFORMATION DISCLOSURE STATEMENT	Applicant David K. Johansen		
FEB 2 0 2004 Page 1 of 1	Filing Date: September 22, 2003	Group: 1725	

Init. Des. Number Class App.	CRADE	MARIA	_	U.S.	Patent Docu	inents		
A02 4,131,505 12/26/1978 Davis, Jr.				Date	Name [.]	Class		Filing Date if App.
A03 4,208,001 06/17/1980 Martner	UE	A01	4,064,462	12/20/1977	Goehler et al.			
A04 4,257,730 03/24/1981 Kawakita		A02	4,131,505	12/26/1978	Davis, Jr.			
A05 4,277,710 07/07/1981 Harwood et al.		A03	4,208,001	06/17/1980	Martner			
A06 4,838,639 06/13/1989 Morankar et al.		A04	4,257,730	03/24/1981	Kawakita			
A07 4,914,290 04/03/1990 Hilgart et al.		A05	4,277,710	07/07/1981	Harwood et al.			
A08 5,295,700 03/22/1994 Crews et al. A09 5,798,599 08/25/1998 Harwood A10 5,880,580 03/09/1999 Johansen Foreign Patent Documents Exam. Ref. Document Number Date Country Class Sub Translation Yes/No Other Art (Including Author, Title, Date Pertinent Pages, Etc.) Exam. Ref. Init. Des. Citation "Hex Buffers/Logic-Level Down Converters High-Performance Silicon-Gate CMOS," Motorola		A06	4,838,639	06/13/1989	Morankar et al.			
A09 5,798,599 08/25/1998 Harwood A10 5,880,580 03/09/1999 Johansen Foreign Patent Documents Exam. Ref. Document Number Date Country Class Sub Translation Yes/No Other Art (Including Author, Title, Date Pertinent Pages, Etc.) Exam. Ref. Des. Citation When Buffers/Logic-Level Down Converters High-Performance Silicon-Gate CMOS," Motorola		A07	4,914,290	04/03/1990	Hilgart et al.			
Foreign Patent Documents Exam. Ref. Document Number Other Art (Including Author, Title, Date Pertinent Pages, Etc.) Exam. Ref. Init. Des. Citation "Hex Buffers/Logic-Level Down Converters High-Performance Silicon-Gate CMOS," Motorola		A08	5,295,700	03/22/1994	Crews et al.			
Foreign Patent Documents Exam. Ref. Document Number Date Country Class Sub Translation Yes/No Other Art (Including Author, Title, Date Pertinent Pages, Etc.) Exam. Ref. Init. Des. Citation "Hex Buffers/Logic-Level Down Converters High-Performance Silicon-Gate CMOS," Motorola	V	A09	5,798,599	08/25/1998	Harwood			
Exam. Ref. Document Date Country Class Sub Class Yes/No Other Art (Including Author, Title, Date Pertinent Pages, Etc.) Exam. Ref. Des. Citation Cit	lo	- A10	5,880,580	03/09/1999	Johansen			
Exam. Ref. Document Number Date Country Class Sub Translation Yes/No Other Art (Including Author, Title, Date Pertinent Pages, Etc.) Exam. Ref. Init. Des. Citation "Hex Buffers/Logic-Level Down Converters High-Performance Silicon-Gate CMOS," Motorola								
Exam. Ref. Document Number Date Country Class Sub Translation Yes/No Other Art (Including Author, Title, Date Pertinent Pages, Etc.) Exam. Ref. Init. Des. Citation "Hex Buffers/Logic-Level Down Converters High-Performance Silicon-Gate CMOS," Motorola								
Other Art (Including Author, Title, Date Pertinent Pages, Etc.) Exam. Ref. Init. Des. "Hex Buffers/Logic-Level Down Converters High-Performance Silicon-Gate CMOS," Motorola				Foreig	n Patent Do	cuments		
Exam. Ref. Des. Citation "Hex Buffers/Logic-Level Down Converters High-Performance Silicon-Gate CMOS," Motorola			1	Date	Country	Class		
Exam. Ref. Des. Citation "Hex Buffers/Logic-Level Down Converters High-Performance Silicon-Gate CMOS," Motorola								
Exam. Ref. Des. Citation "Hex Buffers/Logic-Level Down Converters High-Performance Silicon-Gate CMOS," Motorola						1		
Exam. Ref. Des. Citation "Hex Buffers/Logic-Level Down Converters High-Performance Silicon-Gate CMOS," Motorola		Othe	r Art (Incl	Idina Aut	thor Title D	ata Partir	ent Page	e Etc \
Init. Des. Citation "Hex Buffers/Logic-Level Down Converters High-Performance Silicon-Gate CMOS," Motorola	Fyam							
			Citation					
Semiconductor Technical Data, Motorola, Inc. 1995, 6 pages.	115	_	"Hex Buffers/Logic-Level Down Converters High-Performance Silicon-Gate CMOS," Motorola Semiconductor Technical Data, Motorola, Inc. 1995, 6 pages.					

Examiner	DATE CONSIDERED:	9/5/5
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